What is claimed is:

1. An organic EL drive circuit for current-driving an organic EL panel through terminal pins thereof in a display period according to a first timing control signal having a predetermined frequency for separating the display period corresponding to a scan period of one horizontal line from a reset period corresponding to a retrace period of the horizontal scan, comprising:

a timing signal generator circuit for generating a plurality of second timing control signals sequentially delayed from said first timing control signal;

a reset pulse generator circuit for selecting one of the plurality of said second timing control signals according to said predetermined data and generating a reset pulse having a front edge determined according to said selected second timing control signal and a rear edge determined by said first timing control signal; and

a switch circuit responsive to said reset pulse for connecting said terminal pins to a predetermined bias line to reset said organic EL element of said organic EL panel connected to said terminal pins, luminance of said organic EL panel being regulated by regulating said display period according to said predetermined data.

2. The organic EL drive circuit as claimed in claim 1, wherein the front edge of said reset pulse generated by said reset pulse generator circuit corresponds to a front edge of said selected second timing control signal.

3. The organic EL drive circuit as claimed in claim 2, further comprising a register, wherein said timing signal generator circuit is a delay circuit responsive to said first

timing control signal for generating the plurality of said second timing control signals by delaying said first timing control signal by predetermined times sequentially and said predetermined data is set in said register.

4. The organic EL drive circuit as claimed in claim 3, wherein said reset circuit includes said register and said predetermined data is set in said register externally of said reset circuit.

5. The organic EL drive circuit as claimed in claim 4, wherein said organic EL panel includes a plurality of said organic EL elements for each of R, G and B display colors, said reset pulse generator circuit and said switch circuit are provided for each of R, G and B display colors and said organic EL elements for each display color are reset respectively.

6. The organic EL drive circuit as claimed in claim 5, wherein said switch circuits for each display color are turned ON according to said reset pulse corresponding thereto to connect anodes of said organic EL elements to said predetermined bias line or a bias line dedicated to the related display color.

7. The organic EL drive circuit as claimed in claim 6, wherein said reset pulse generator circuit is provided for each of said terminal pins and said predetermined data is set correspondingly to said terminal pins.

8. The organic EL drive circuit as claimed in claim 7, wherein said front edge is a rising edge, said rear edge is a falling edge, said delay circuit is constructed with a shift register, said reset pulse generator circuit comprises said register, a selector and an AND or a NAND circuit, said

selector selects one of the plurality of said second timing control signals according to said predetermined data and said AND or NAND circuit generates said reset pulse according to said first timing control signal and said selected second timing control signal.

9. The organic EL drive circuit as claimed in claim 8, further comprising a D/A converter circuit provided for each of said terminal pins and a current source provided for each of said terminal pins for current-driving said terminal pins, said D/A converter circuit driving said current source according to the current obtained by the D/A conversion.

10. An organic EL display device for current-driving an organic EL panel through terminal pins thereof in a display period according to a first timing control signal having a predetermined frequency for separating the display period corresponding to a scan period of one horizontal line from a reset period corresponding to a retrace period of the horizontal scan, comprising:

a timing signal generator circuit for generating a plurality of second timing control signals sequentially delayed from said first timing control signal;

a reset pulse generator circuit for selecting one of the plurality of said second timing control signals according to said predetermined data and generating a reset pulse having a front edge determined according to said selected second timing control signal and a rear edge determined by said first timing control signal; and

a switch circuit responsive to said reset pulse for connecting said terminal pins to a predetermined bias line to reset said organic EL element of said organic EL panel

connected to said terminal pins, luminance of said organic EL panel being regulated by regulating said display period according to said predetermined data.

11. The organic EL display device claimed in claim 10, further comprising a register, wherein said timing signal generator circuit is a delay circuit responsive to said first timing control signal for generating the plurality of said second timing control signals by delaying said first timing control signal by predetermined times sequentially and said predetermined data is set in said register.

12. The organic EL display device as claimed in claim 11, wherein said reset circuit includes said register and said predetermined data is set in said register externally of said reset circuit.

13. The organic EL display device as claimed in claim 12, wherein said organic EL panel includes a plurality of said organic EL elements for each of R, G and B display colors, said reset pulse generator circuit and said switch circuit are provided for each of R, G and B display colors and said organic EL elements for each display color are reset respectively.

14. The organic EL display device as claimed in claim 13, wherein said front edge is a rising edge, said rear edge is a falling edge, said switch circuits for each display color are turned ON according to said reset pulse corresponding thereto to connect anodes of said organic EL elements to said predetermined bias line or a bias line dedicated to the related display color.

15. An organic EL drive circuit for current-driving an organic EL panel through terminal pins thereof in a display

period according to a timing control signal having a predetermined frequency for separating the display period corresponding to a scan period of one horizontal line from a reset period corresponding to a retrace period of the horizontal scan, comprising:

a timing signal generator circuit for generating a plurality of timing signals sequentially delayed from said timing control signal;

a reset pulse generator circuit for selecting one of the plurality of said timing signals according to said predetermined data and generating a reset pulse having a front edge determined according to said selected timing signal and a rear edge determined by said timing control signal; and

a switch circuit responsive to said reset pulse for connecting said terminal pins to a predetermined bias line to reset said organic EL element of said organic EL panel connected to said terminal pins, luminance of said organic EL panel being regulated by regulating said display period according to said predetermined data.

16. The organic EL drive circuit as claimed in claim 15, wherein said timing signals are generated by delaying said timing control signal.

17. An organic EL display device for current-driving an organic EL panel through terminal pins thereof in a display period according to a first timing control signal having a predetermined frequency for separating the display period corresponding to a scan period of one horizontal line from a reset period corresponding to a retrace period of the horizontal scan, comprising:

a timing signal generator circuit for generating a plurality of timing signals sequentially delayed from said timing control signal;

a reset pulse generator circuit for selecting one of the plurality of said timing signals according to said predetermined data and generating a reset pulse having a front edge determined according to said selected timing signal and a rear edge determined by said timing control signal; and

a switch circuit responsive to said reset pulse for connecting said terminal pins to a predetermined bias line to reset said organic EL element of said organic EL panel connected to said terminal pins, luminance of said organic EL panel being regulated by regulating said display period according to said predetermined data.